



SPECIFICATIONS 产品规格书

MODULE NO.(产品型号): PV04015H0150P

Customer Name: (客户名称)	
Customer P/N: (客户型号)	
Data: (日期)	2022-10-10
Version: (版本)	V1. 0

Customer Approval(客户承认)

Prepare(制作)	Check(审核)	Approval(核准)



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Revision History(修订历史)

REV	REVISED DESCRIPTIONS (修订描述)	DATE (日期)
V1.0	Generation first version	2022-10-10





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1 LCD MODULE PHYSICAL DATA

1.1 Features

Display Type	TFT
Viewing Direction	ALL
Connection Type	COG + FPC+BL
Driving IC	ST7701S
MPU interface	RGB
Backlight	8 pcs LED

Table 1.

1.2 <u>Mechanical Specification</u>

Item	Standard Value	Unit
Screen size	4.0	inch
Number of dots	480RGB x 480dots	pixel
LCM dimension	75.66(H) x78.23(V) x2.54(T)	mm
Active area	71.86(H) x70.18(V)	mm
Pixel pitch	0.1497 (H) x 0.1462 (V)	mm
Approx. weight	TBD	g

Table 2.





2 OUTLINE DIMENSIONS

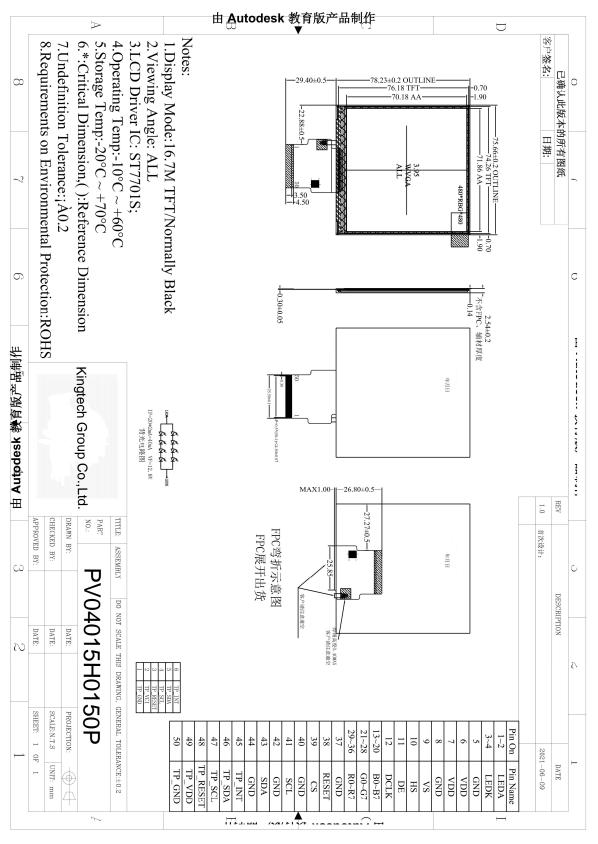


Figure 1.



3 ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITIO	STA	UNIT		
	5111202	N	MIN	TYP	MAX	01.11
System Voltage	VDD	Ta=+25℃	-0.3	-	4.6	V
CTP System Voltage	TP_VDD	Ta=+25℃	-0.3	-	3.6	V
Input Voltage	Vin	Ta=+25°C	-0.3	-	VDD+0.3	V

Table	3.
-------	----

NOTE:

If one of the above items is exceeded its maximum limitation momentarily, the quality of the product may be degraded. Absolute maximum limitation, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the recommend range.



4 ELECTRICAL CHARACTERISTICS

4.1 DC Characteristics

ITEM	SYMBOL	CONDITIONS	STAN	UNIT		
			MIN	TYP	MAX	
System Voltage	VDD	$Ta = +25^{\circ}C$	2.5	2.8	3.3	V
Input High Voltage for LCD	VIH	_	0.7Iovc c	_	Iovcc	V
Input Low Voltage for LCD	VIL	_	Vss	_	0.3 Iovec	V
Output High Voltage for LCD	VOH	_	0.8Iovc c	_	Iovcc	V
Output Low Voltage for LCD	VOL	_	Vss		0.2Iovcc	V

Table 4.

NOTE:VDD=Iovcc=2.8V, VSS= 0V, TA=25° C

4.2 Back-Light unit

PARAMETER	SYMBOL	REMARK	STA	UNIT		
			MIN	TYP	MAX	
FORWARD VOLTAGE	VF	lf =40mA		12.8	-	V
LUMINOUS INTENSITY	lv	lv lf =40mA		TBD		cd/m2
LUMINOUS UNIFORMITY	lv-m	(min/max)/100	-	80	-	%
CHROMATICITY COORDINATES	Х	lf =40mA	0.260	-	0.330	
CHROMATICITY COORDINATES	Y	II =40MA		-	0.330	
OPERATING TEMPERATURE	-10°C ~60°C					
STORAGE TEMPERATURE	-20 ℃ ~70℃					

Table 5.

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4.3 POWER ON/OFF SEQUENCE

VDDI and VDDA can be applied or powered down in any order. During the Power Off sequence, if the LCD is in the Sleep Out mode, VDDA and VDDI must be powered down with minimum 120msec. If the LCD is in the Sleep In mode, VDDA and VDDI can be powered down with minimum 0msec after the RESX is released. CSX can be applied at any timing or can be permanently grounded. RESX has high priority over CSX. Notes:

- 1. There will be no damage to the ST7701S if the power sequences are not met.
- 2. There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.
- 3. There will be no abnormal visible effects on the display between the end of Power On Sequence and before
- receiving the Sleep Out command, and also between receiving the Sleep In command and the Power Off Sequence.

4. If the RESX line is not steadily held by the host during the Power On Sequence as defined in Sections 9.1 and 9.2, then it will be necessary to apply the Hardware Reset (RESX) after the completion of the Host Power On Sequence to ensure correct operations. Otherwise, all the functions are not guaranteed. The power on/off sequence is illustrated below

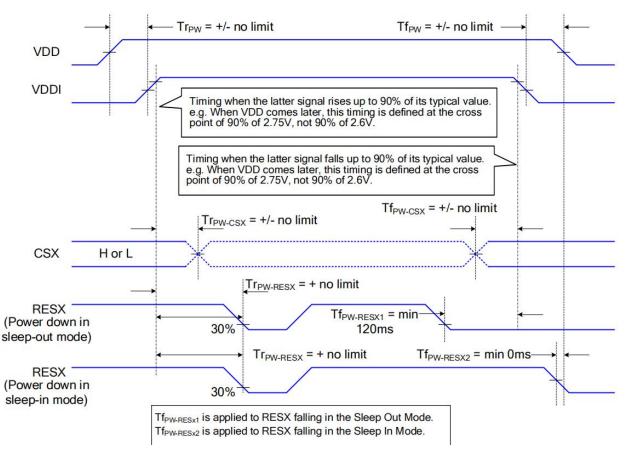


Figure 2



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<u>4.4 RGB Input Taming table</u>

(VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 °C)

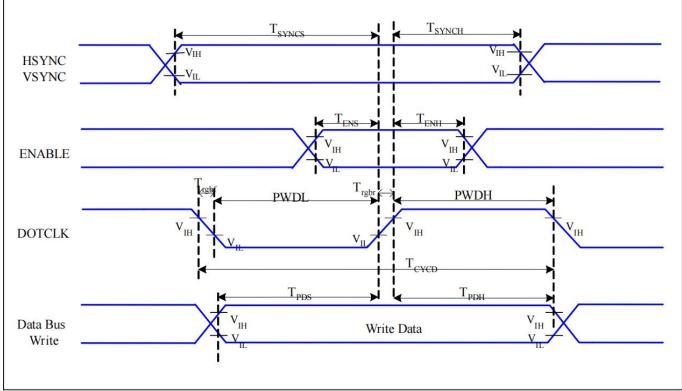


Figure 3.RGB Interface Timing Characteristics

Signal	Symbol	Parameter MIN MAX Unit				Description
HSYNC, VSYNC	TSYNCS	VSYNC, HSYNC Setup Time	5	-	ns	
ENABLE	T _{ENS}	Enable Setup Time	5	-	ns	
ENABLE	T _{ENH}	Enable Hold Time	5	-	ns	
	PWDH	DOTCLK High-level Pulse Width	15	8	ns	
DOTCLK	PWDL	DOTCLK Low-level Pulse Width	15	-	ns	
DOTCLK	TCYCD	DOTCLK Cycle Time	33	14	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time		15	ns	
DP	T _{PDS}	PD Data Setup Time	5	18	ns	
DB	T _{PDH}	PD Data Hold Time	5	-	ns	

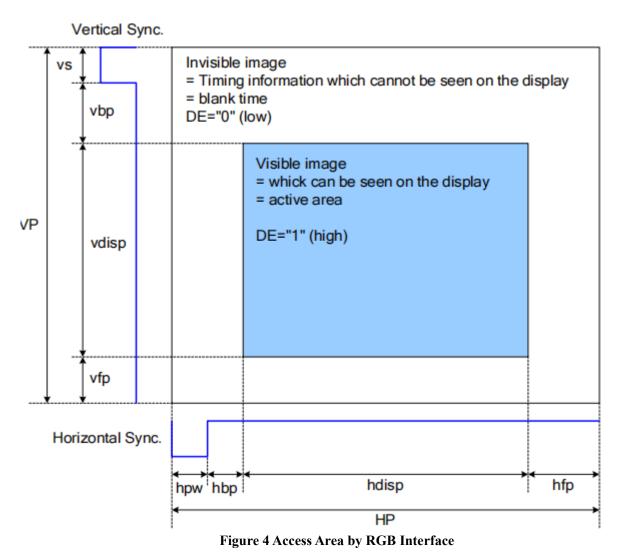
Table 6 18/16 Bits RGB Interface Timing Characteristics





4.5 **RGB Interface Definition**

The display operation via the RGB interface is synchronized with the VSYNC, HSYNC, and DOTCLK signals. The data can be written only within the specified area with low power consumption by using window address function. The back porch and front porch are used to set the RGB interface timing.



Note:

Typical value are related to the setting frame rate is 60Hz.



1 ELECTRO-OPTICAL CHARACTERISTICS

Para	mete	Symbol	Condition	Min	Тур	Max	Unit	Remark
Threshol	Threshold voltage			2.7	3.3	3.3	V	Note 1
		Vth		1.2	1.5	1.8	V	Note I
	Horizontal	Left(9')		-	80	-	Deg	
Viewing H Angle range	Horizoniai	Right(3')	CR > 10	-	80	-	Deg	Not 2
	Vertical	Up(12')	CK > 10	-	80	-	Deg	Not 2
	vertical	Down(6')		-	80	-	Deg	
Contra	st ratio	C/R	$\Theta = 0^{\circ}$	-	800	-		Not 3
Transm	nittance	T(%)	$\Theta = 0^{\circ}$	-	4.2	-		Not 4
White Ch		XW	$\Theta = 0_{\circ}$	-	0.308	-		
white Ch	romaticity	yw		-	0.337	-		
	Red	xR		-	0.608	-		
	Red	yR		-	0.323	-		Not 5 *Color
Reproductio	on Green	xG	$\Theta = 0^{\circ}$	-	0.317	-		Filter Glass
Of color	Green	yG	$\Theta = 0^{-1}$	-	0.549	-		Giubb
	D111-	xB		-	0.145	-		
	Blue	yВ		-	0.138	-		
Respon	se Time	Tr+Tf	$\Theta = 0^{\circ}$		30		smec	Not 6







Note :

1. The definition of Vth & Vsat

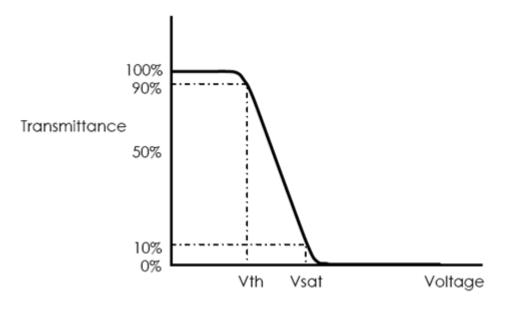


Figure 6. The definition of Vth & Vsat

2 2.Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.

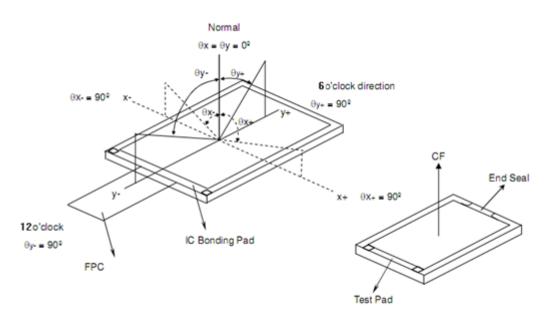


Figure 7.Definition of viewing angle

3. Contrast measurements shall be made at viewing angle of $\Theta = 0^{\circ}$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state.





 $CR \Box \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$

4. Transmittance is the value with Polarizer.

5. The color chromaticity coordinates specified in Table 6. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the C/F. Measurement condition is C - light source & Halogen Lamp.

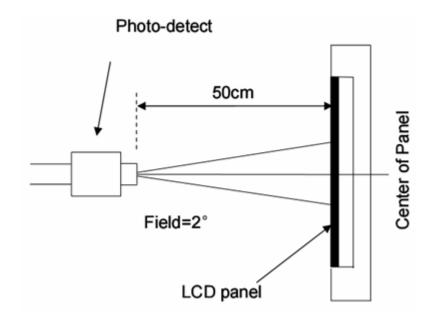
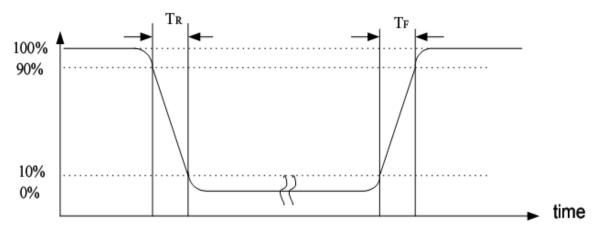
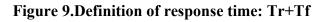


Figure 8 Optical test equipment.

6. The electro-optical response time measurements shall be made as FIGURE 3 shown in Appendix by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td









3 INTERFACE PIN CONNECTIONS

PIN NO.	SYMBOL	FUNCTION DESCRIPTIONS		
1-2	LEDA	Backlit positive		
3-4	LEDK	Backlit negative		
5	GND	Ground		
6-7	VCI	System Voltage		
8	GND	Ground		
9	VS	Frame synchronizing signal for RGB interface operation		
10	HS	Line synchronizing signal for RGB interface operation		
11	DE	Data enable signal for RGB interface operation Low: access enabled High: access inhibited		
12	DCLK	Dot clock signal for RGB interface operation		
13~20	B0~B7	data bus		
21~28	G0~G7	data bus		
29~36	R0~R7	data bus		
37	GND	Ground		
38	RESET	Reset Signal		
39	CS	A chip select signal Low: the chip is selected and accessible High: the chip is not selected and not accessible		
40	GND	Ground		
41	SCL	Serial clock input for SPI interface.		
42	GND	Ground		
43	SDA	Serial data input/output bidirectional pin for SPI Interface.		
44	GND	Ground		
45	TP INT	INTERUPT SIGNAL		
46	TP SDA	IIC DATA		
47	TP SCL	IIC CLOCK		
48	TP RESET	TP RESET SIGNAL		
49	TP VDD	CTP POWER SUPPLY		
50	TP_GND	Ground		

Table 8.





4 INITIALIZED CODE

Please contact us for details.



5 SPECIFICATION OF QUALITY ASSURANCE

5.1 Summary

The customer should check and accept the products of Kingtech within one month after reception. This standard for Quality Assurance should affirm the quality of LCD products to supply to purchaser by Kingtech Group Co.,Ltd. Entire process is controlled according to QS9000.

5.2 Standard for quality test

(1) Inspection

Before delivering, the supplier should take the following tests, and affirm the quality of product.

(2) Electro-Optical Characteristics

According to the individual specification to test the product.

(3) Test of Appearance Characteristics:

According to the individual specification to test the product.

(4)Test of Reliability Characteristics

According to the definition of reliability on specification for test product.

(5) Delivery Test

Before delivering, the supplier should take the delivery test

(6)Sampling Method: GB/T2828.1-2003, Level II

(7) The defects classify of AQL as following

Major defect: AQL=0.65

Minor defect: AQL=1.5

5.3 Nonconforming Analysis & Deal With Manners

 \therefore Nonconforming Analysis

(1) Purchaser should supply the detail data of nonconforming sample and the non-suitable state.

(2) After accepting the detail data from purchaser ,the analysis of nonconforming should be finished in two weeks.

(3) If supplier can not finish analysis on time ,must announce purchaser before two weeks.

 $\stackrel{\wedge}{\sim}$ Disposition of nonconforming

(1) If find any supplier defect during assembly line, supplier must change the good product for every defect after recognition.

(2) Both supplier and customer should analysis the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.



5.4 Agreement items.

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Both sides should discuss together when the following problems happen:

(1) There is any problem of standard of quality assurance ,and both sides think that must be modifier.

(2) There is any argument item which does not record in the quality assurance.

(3) Any other special problem.

5.5 Standard of the Product Appearance Test

5.5.1 Manner of appearance test

(1) The test must be under 20W*2 or 40W fluorescent light , and the distance of view must be at 30 ± 5 cm.

(2) When test the model of Transmissive product must add the reflective plate.

(3) Definition of Area:

A Area: Active area

B Area: Viewing area

C Area: Out of viewing area

D Area: Seal area

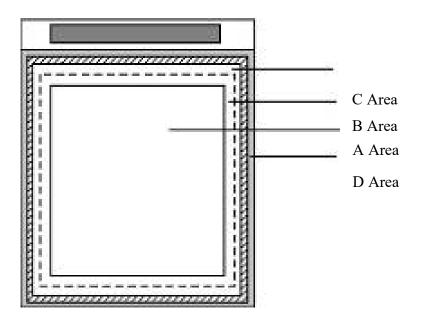


Figure9

5.5.2 Basic principle:

(1) It will accord to the AQL when the standard can not be described.





- (2) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- (3) Must add new item on time when it is necessary.

5.6 Inspection specification

NO	Item	Criterion	AQL
01	Electrical Testing	 1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Contrast defect 	0.65



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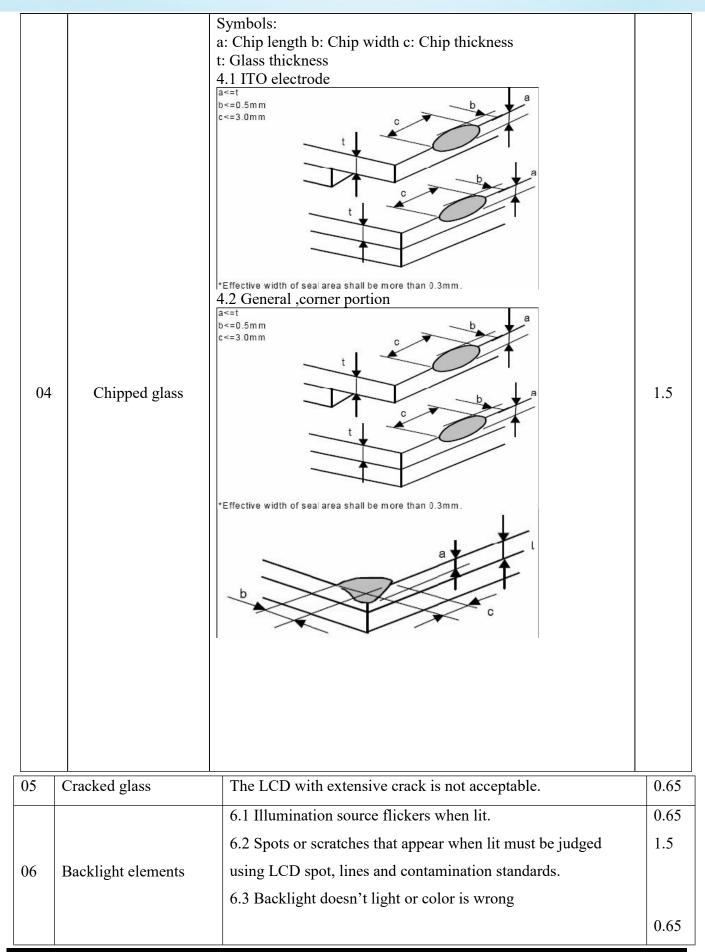
		2.1Round $\varphi = (x+y)/2$		llowing drav	wing			
		Size		eptable QTY	R	emark		
		$\phi \leq 0.10$	A.A Ignore	V.A e Ignore				
		φ=0.10 0.10<φ≦ 0.15		3	No m than t			
	LCD black spots, white spots, color	$ \begin{array}{c c} 0.15 \\ \leq \phi \leq 0.1 \\ 0 \end{array} $	2 1	2	spots	spots within		
	spots,	0.20<φ	0	0				
02	contamination,	Total	3	5				1.5
	y)	2.2 Line Type: (As following drawing) \downarrow \downarrow \downarrow W L \downarrow \downarrow \downarrow W Length Width Acceptable QTY Remark						
				A.A	V.À			
			$W \leq 0.03$	Ignore	Ignore			
		L≦2.5	0.03< W≦0.05	2	3	No more than		
		L≦1.5	0.05<			two lines within 5mm		
			W≦0.08 0.08< W	0	0	within 5mm	-	
			0.00 • 11					
				e, judge usin eck in specif		oot specificati n.	ion, not	
	Polarizer bubbles Ignore	Size		Acceptable QT				
				A. A	V. A	V. A		1.5
03	l Ignore]	lgnore	Ignore			
03	Ignore	φ≦0.15		-	-			
03	Ignore	φ≦0.15 0.15<φ≦	≦0.2 Ž	2	3 0			



Professional LCD Module Manufacturer since 2003

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		7.1 No unmelted solder paste may be present on the PCB.	1.5	
07		7.2 No cold solder joints, missing solder connections, oxidation		
	Saldanina	or icicle.		
	Soldering	7.3 No residue or solder balls on PCB.		
		7.4 No short circuits in components on PCB.		
		8.1 No oxidation, contamination, curves or, bends on interface	1.5	
		pin (OLB) of TCP.		
		8.2 No cracks on interface pin(OLB) of TCP		
		8.3 NO contamination, solder residue or solder balls on		
		product.		
		8.4 The IC on the TCP may not be damaged, circuits.		
		8.5 The residual rosin or tin oil of soldering (component or chip	1.5	
	General appearance	component) is not burned into brown or black color. 8.6		
		Sealant on top of the ITO circuit has not hardened	1.5	
08		8.7 Pin type must match type in specification sheet.		
		8.8 LCD pin loose or missing pins.	0.65	
		8.9 Product packaging must the same as specified on packaging	0.65	
		specification sheet.		
		8.10 Product dimension and structure must conform to product	0.65	
		specification sheet.		

Table 8.

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6 RELIABILITY

NO	Test Item	Description	Test Condition	
1	High temperature storage	Endurance test applying the high storage temperature for a long time	70°С,24 Н	
2	Low temperature storage	Endurance test applying the low storage temperature for a long time	-20°C,24H	
3	High temperature operation	Endurance test applying the electric stress under high temperature for a long time	60°C,24H	
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time	-10°C,24H	
5	High temperature /humidity storage	Endurance test applying the high temperature and high humidity storage for a long time	50°C,90% R.H 24H	
6	High temperature /humidity operation	Endurance test applying electric stress under high temperature and high humidity for a long time	40°C 90% R.H 24H	
7	Temperature Cycle	Endurance test applying the low and high temperature cycle $-20^{\circ}C \rightarrow 25^{\circ}C \rightarrow 70^{\circ}C$ $\rightarrow 25^{\circ}C$ 30min 5min 30min 5min one cycle	-10°C/60°C 10 cycles	
8	Vibration test	Endurance test applying the vibration during transportation and using	10Hz~50Hz Swing:0.75mm time:30min	
9	Fall test	Endurance test dropping the LCM from a high place	600mm height	
10	Static electricity test	Endurance test applying static electric stress to terminal	Contact discharge: ±2KV~4KV Air discharge: ±2KV~8KV	

Table 9.

NOTE: TEST CONDITION

- (1) Temperature and humidity: If no specification, temp. set at $25\pm2^{\circ}$ C, humidity set at $60\pm5\%$ RH.
- (2) Operating state: Samples subject to the test shall be in "operating" condition.



7 USING LCD MODULES

7.1 LIQUID CRYSTAL DISPLAY MODULES

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- (2) Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
- (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.
- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).
- (10) As glass is fragile. It tends to become or chipped during handling especially on the edges.Please avoid dropping or rising.

7.2 PRECAUTION FOR HANDING LCD MODULES

- Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (1) Do not alter, modify or change the the shape of the tab on the metal frame.
- (2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- (3) Do not damage or modify the pattern writing on the printed circuit board.
- (4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- (5) Except for soldering the interface, do not make any alterations or modifications with a soldering





iron.

(6) Do not drop, bend or twist LCM.

(7) In order to avoid the cracking of the FPC, you should to pay attention to the area of FPC(R50mm) where the FPC was bent .the edge of coverlay; the area of surface of Ni-Au plating, the area of soldering land, the area of through hole.

7.3 ELECTRO-STATIC DISCHARGE CONTROL

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- (1) Make certain that you are grounded when handing LCM.
- (2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- (6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 0%-60% is recommended.

7.4 PRECAUTIONS FOR OPERATION

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
- (2) Driving the LCD in the voltage above the limit shortens its life.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.
- (6) When turning the power on, input each signal after the positive/negative voltage becomes stable.



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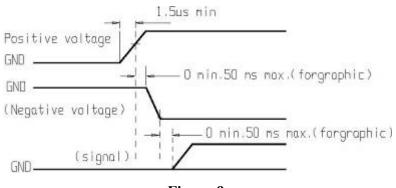


Figure 9.

7.5 STORAGE

When storing LCDs as spares for some years, the following precaution are necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)
- (4) Environmental conditions :
- Do not leave them for more than 160hrs. at 70°C.
- Should not be left for more than 48hrs. at -20°C.

7.6 SAFETY

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leakes out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and ater.

7.7 LIMITED WARRANTY

Unless agreed between Kingtech and customer, Kingtech will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with Kingtech LCD modules acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to Kingtech within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Kingtech limited to repair and/or replacement on the terms set forth above. Kingtech will not be responsible for any subsequent or consequential events.





7.8 RETURN LCM UNDER WARRANTY

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- Circuit modified in any way, including addition of components.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB's eyelet, conductors and terminals.





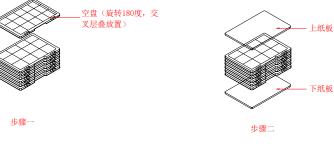
12. 包装方式 (PACKING MODE) 仅供参考详情下单后再定

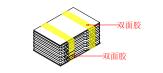
1 包装规范

- 1.1 装箱办法见《包装方法示意图》。装箱数量则由每一个吸塑盘所装的数量决定。每箱装一叠吸塑, 每叠11个吸塑盘,上面一个吸塑盘不装产品,交叉层叠放置,上下需用纸板、胶纸固定。
- 1.2 小箱:材料为K3K,外腔尺寸为485*355*130mm。
- 1.3 吸塑盘: PET 透防静电或着 PS 黑防静电材料,厚度为 0.6MIN,外形尺寸(根据背光供应商提供 吸塑盘尺寸为准),用量为每箱 11Pcs。
- 1.4 纸板:材料为 A-A 瓦楞纸板,用量为每箱 2Pcs。
- 1.5 装箱数量的计算:

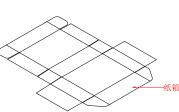
每个吸塑盒所装数量*10(层)

每个吸塑盘装产品数量 10,每箱的装箱数量为:10*10=100 (Pcs)产品. 小箱包装示意图:

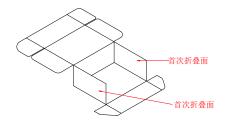




步骤三

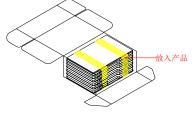


步骤四



步骤五

步骤七



步骤六

步骤八



2 按产品型号及物料编号贴上标签及盖上 PASS 章 正常情况下,使用统一的标签。部分产品有写上客户的型号或物料编号按"产品型号及客户

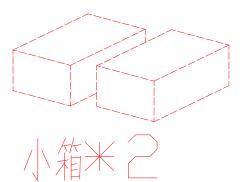


物料对应表"进行填写。特殊专用的产品标签及出港或走快递的产品,包装标签按跟单人员提供的 进行标明。

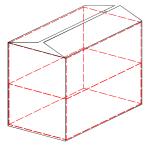
出港及走快递的产品,需要外箱 3

KINGTECH

出港及走快递的产品建议用大小箱包装,即两个小箱加外箱。(如图)







~ 箱+ 小箱米 2 h

NO.	Item	Dimensions	Quantity	Remark
1	TRAY	One tray	10	
2	SMALL CARTON	One carton/11 tray	100	
3	LARGE CARTON	One carton/2 small carton	200	

打包作业流程图 4

